

PRODUCT INFORMATION PACKET



Model No: EX141159.00

Catalog No: EX141159.00

EX141159.00..10/7.5HP..1755/1465RPM.215TZ.TEFC.208-230/460//190/380V.3PH.60/50HZ.CONT.40C.1.15SF.RI
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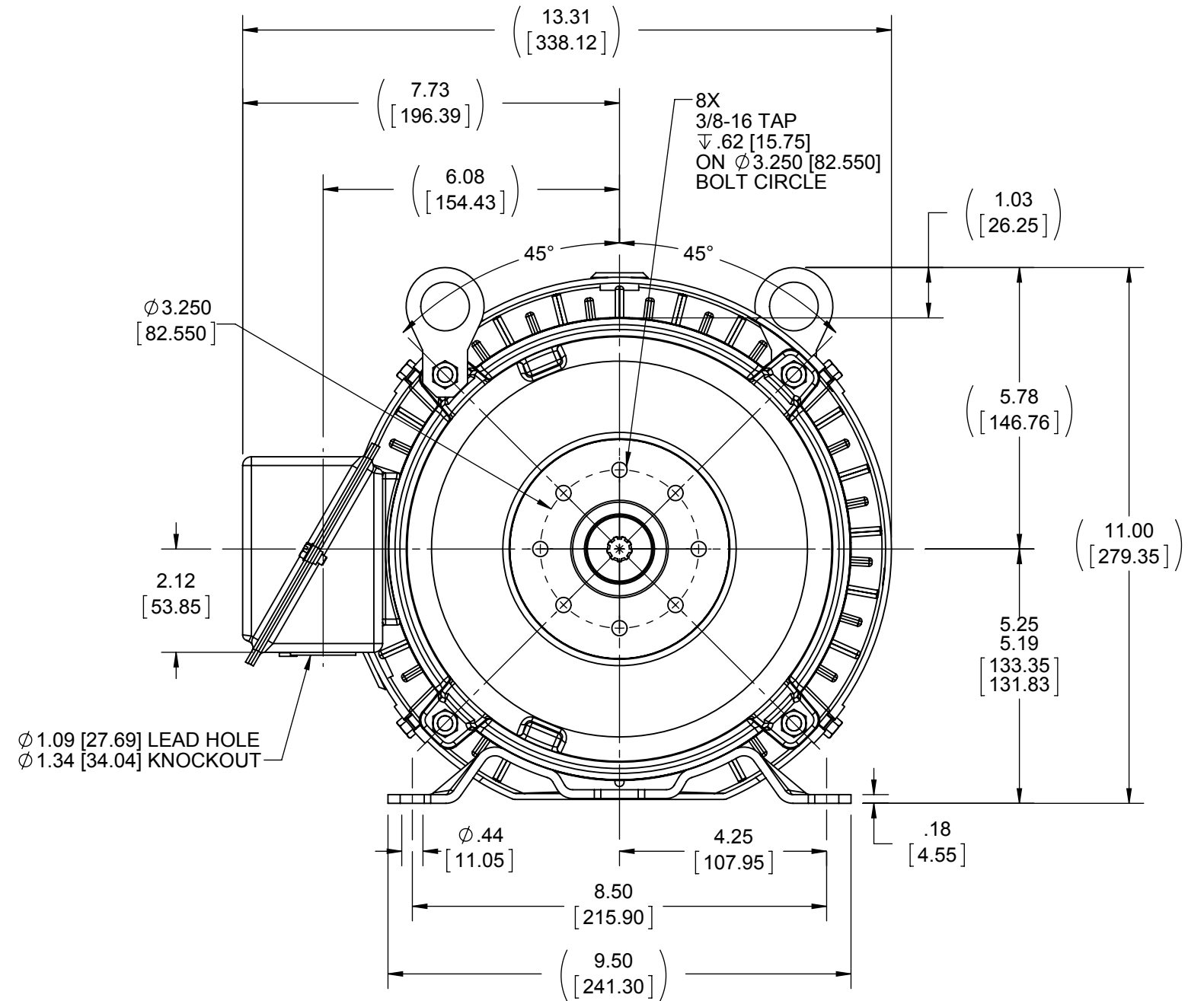
Nameplate Specifications

Phase	3	Output HP	10 & 7.50 Hp
Output KW	7.5 & 5.6 kW	Voltage	208-230/460 & 190/380 V
Speed	1755 & 1465 rpm	Service Factor	1.15 & 1.15
Frame	215TZ	Enclosure	Totally Enclosed Fan Cooled
Thermal Protection	No Protection	Efficiency	89.5 & 91 %
Ambient Temperature	40 °C	Frequency	60 & 50 Hz
Current	28-26/13 & 23/11.5 A	Power Factor	81
Duty	Continuous	Insulation Class	F
Design Code	B	KVA Code	G
Drive End Bearing Size	6307	Opp Drive End Bearing Size	6206
UL	Recognized	CSA	Y
CE	N	IP Code	43
Number of Speeds	1		

Technical Specifications


Electrical Type	Squirrel Cage Inverter Rated	Starting Method	Line Or Inverter
Poles	4	Rotation	Reversible
Resistance Main	1.05 Ohms	Mounting	Rigid Base
Motor Orientation	Horizontal	Drive End Bearing	Ball
Opp Drive End Bearing	Ball	Frame Material	Rolled Steel
Shaft Type	Single Special Extension	Assembly/Box Mounting	F1 ONLY
Inverter Load	CONSTANT 10:1		
Outline Drawing	609-0006	Connection Drawing	A-EE7308-LE

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DRAWING REVISION C	REVISION BY MITCH VERBICK	DATE 10/7/2014
ECO ECO-0062088	APPROVED BY	DATE
ECO DESCRIPTION		
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DRAWN BY MITCH VERBICK	
DATE	2/18/2014
APPROVED BY	
DATE	
PROCESS/FINISH	
THIRD ANGLE PROJECTION	


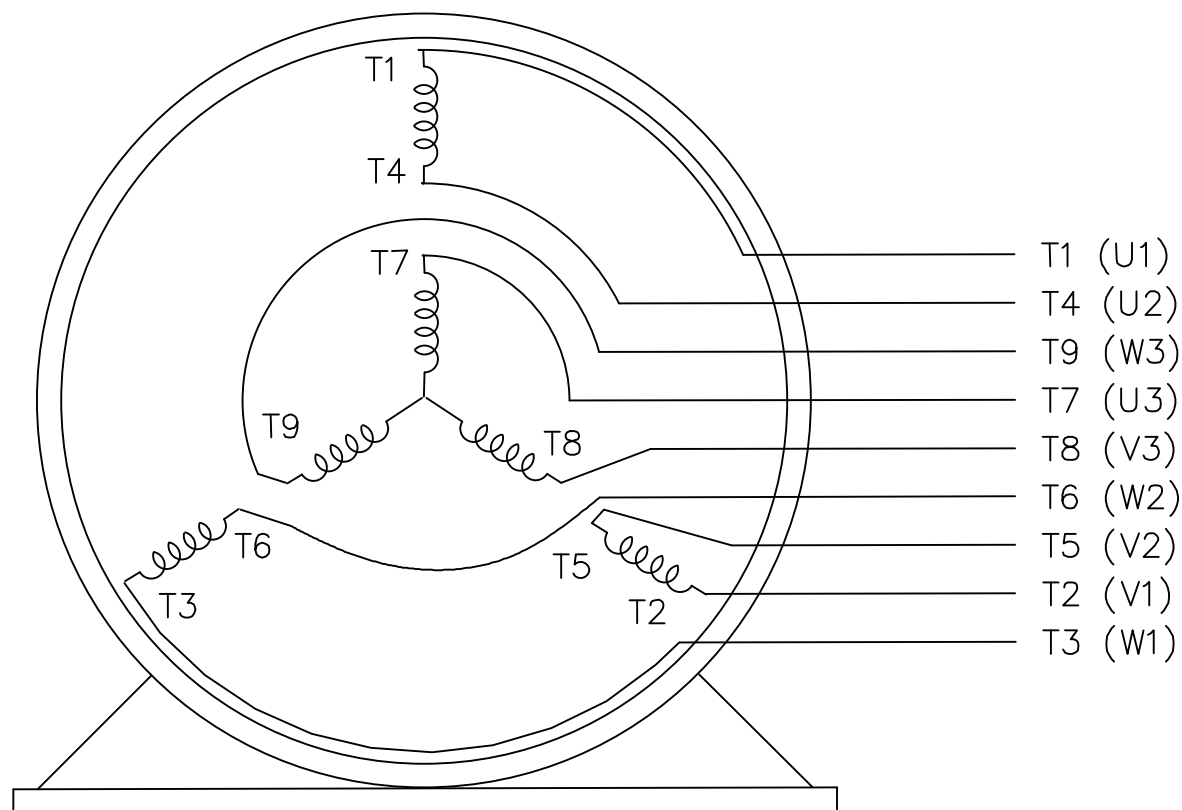
 Regal Beloit America, Inc.		
DESCRIPTION		
OUTLINE 210 FRAME SAE AA SPLINE		
MATERIAL		
SIZE	DRAWING NUMBER	SHEET
B	609-0006	1 OF 1

Diagram illustrating a quantum circuit with 9 qubits (T1 to T9) and 3 classical outputs (L1, L2, L3). The qubits are grouped into three sets of three, each connected to a classical output:

- Qubits (U1) T1, (V1) T2, and (W1) T3 are connected to L1.
- Qubits (U2) T4, (U3) T7, and (V2) T5 are connected to L2.
- Qubits (V3) T8, (W2) T6, and (W3) T9 are connected to L3.

Diagram illustrating a 3-to-1 multiplexer structure with three 2-to-1 sub-multiplexers:


- Sub-multiplexer 1: Inputs (U1) T1 and (U3) T7; Output L1.
- Sub-multiplexer 2: Inputs (V1) T2 and (V3) T8; Output L2.
- Sub-multiplexer 3: Inputs (W1) T3 and (W3) T9; Output L3.
- Sub-multiplexer 4: Inputs (U2) T4 and (V2) T5; Output L3.
- Sub-multiplexer 5: Inputs (W2) T6 and (U2) T4; Output L3.



VIEW OF TERMINAL END

REF.
WINDING DIAGRAM

T8Y, T2Y, T2BL, T4BX, T2EC, T2G
T6BZ, T2B, T6BL, T4AV, T6B, T4B

				TOLERANCES UNLESS SPECIFIED		 ELECTRIC MOTORS GEARMOTORS AND DRIVES	DRAWN HLB 04-29-2002		
				DEC.	INCHES		CHK ML 05-03-2002		
				.X	±.1		APPD GK 05-03-2002		
				.XX	±.01		SCALE 1=1		
2	ADDED IEC NOTATIONS... (U1), (V1) ETC. (MU105786)	REP 01-11-2012	DR	.XXX	±.005	TITLE CONNECTION DIAGRAM 3ø – DUAL VOLTAGE MOTOR	REF		
1	NEW DRAWING	HLB 05-03-2002	ML	.XXXX	±.0005	MAT'L.	FMF		
NO.	REVISION	BY & DATE	CHK	ANG	±1/2"	FINISH	PREV		
THIS DRAWING IN DESIGN AND DETAIL IS OUR PROPERTY AND MUST NOT BE USED EXCEPT IN CONNECTION WITH OUR WORK ALL RIGHTS OF DESIGN AND INVENTION ARE RESERVED THIS IS AN ELECTRONICALLY GENERATED DOCUMENT – DO NOT SCALE THIS PRINT				RFP		CAD FILE EE7308-LE	SIZE A	DRAWING NO. EE7308-LE	PAGE OF 2
				DIST LB-WP					