

# PRODUCT INFORMATION PACKET



Model No: 199744.00  
Catalog No: 199744.00  
20,3600,DP,254TC,3/60/230/460

Regal and Leeson are trademarks of Regal Rexnord Corporation or one of its affiliated companies.  
©2023 Regal Rexnord Corporation, All Rights Reserved. MC017097E





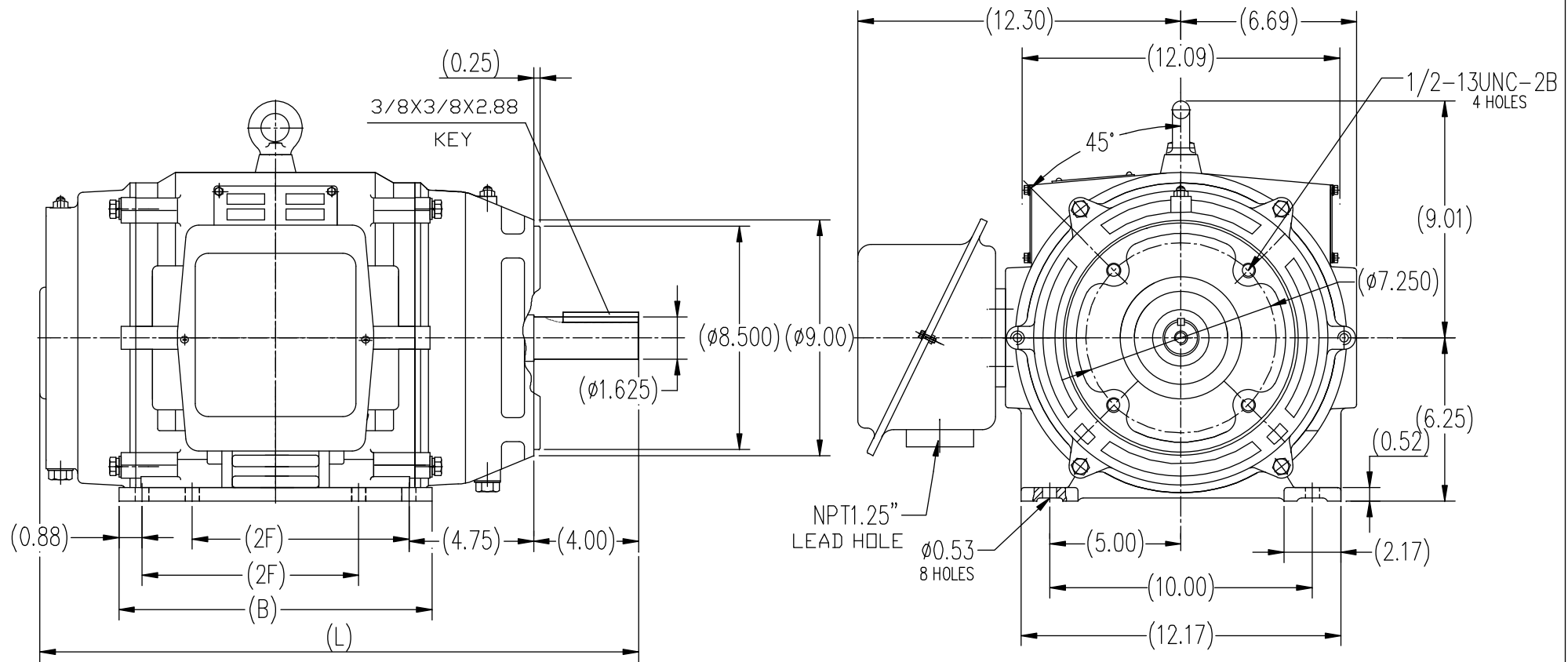
### Nameplate Specifications

Phase	3	Output HP	20 & 15 Hp
Output KW	14.9 & 11.2 kW	Voltage	230/460 & 190/380 V
Speed	3540 & 2945 rpm	Service Factor	1.15 & 1.15
Frame	254TC	Enclosure	Drip Proof
Thermal Protection	No Protection	Efficiency	91.7 & 91.7 %
Ambient Temperature	40 °C	Frequency	60 & 50 Hz
Current	45/22.5 & 41/20.5 A	Power Factor	89.5
Duty	Continuous	Insulation Class	F
Design Code	B	KVA Code	G
Drive End Bearing Size	6209	Opp Drive End Bearing Size	6208
UL	Recognized	CSA	N
CE	Y	IP Code	22
Number of Speeds	1		

### Technical Specifications

Electrical Type	Squirrel Cage Inverter Rated	Starting Method	Line Or Inverter
Poles	2	Rotation	Reversible
Resistance Main	.552 Ohms	Mounting	Rigid Base
Motor Orientation	Horizontal	Drive End Bearing	Ball
Opp Drive End Bearing	Ball	Frame Material	Cast Iron
Shaft Type	T	Assembly/Box Mounting	F1/F2 CAPABLE
Inverter Load	VARIABLE 10:1		
Outline Drawing	SS620658	Connection Drawing	EE7308-LE

This is an uncontrolled document once printed or downloaded and is subject to change without notice. Date Created:06/22/2023



254TC	8.25	12.00	23
256TC	10.00	13.59	24.6
FRAME	2F	BB	C

		TOLERANCES UNLESS SPECIFIED		REGAL Regal-Beloit Corporation	DRAWN ZYH 11-05-2013	
		DEC.	INCHES		CHK	
		.X	±.1	TITLE OUTLINE 254/256TC FR-ODP-CAST IRON	APPD	
		.XX	±.03		SCALE	1=6
		.XXX	±.005		REF	
		.XXXX	±.0005	MAT'L.	FMF	HWADA
NO.	REVISION	BY & DATE	CHK	ANG	FINISH	PREV
THIS DRAWING IN DESIGN AND DETAIL IS OUR PROPERTY AND MUST NOT BE USED EXCEPT IN CONNECTION WITH OUR WORK. ALL RIGHTS OF DESIGN AND INVENTION ARE RESERVED. THIS IS AN ELECTRONICALLY GENERATED DOCUMENT - DO NOT SCALE THIS PRINT				RFP	CAD FILE	SS620658
				DIST	SIZE	DRAWING NO.
					B	SS620658
						REV.

Diagram illustrating a quantum circuit with 9 qubits (T1 to T9) and their corresponding labels (U1) T1, (V1) T2, (W1) T3, (U2) T4, (U3) T7, (V2) T5, (V3) T8, (W2) T6, and (W3) T9. The circuit consists of three layers of gates:

- Layer 1: CNOT gates from T1 to T2, T2 to T3, and T4 to T7.
- Layer 2: CNOT gates from T5 to T6, T6 to T8, and T9 to T7.
- Layer 3: CNOT gates from T1 to T2, T2 to T3, and T4 to T7.

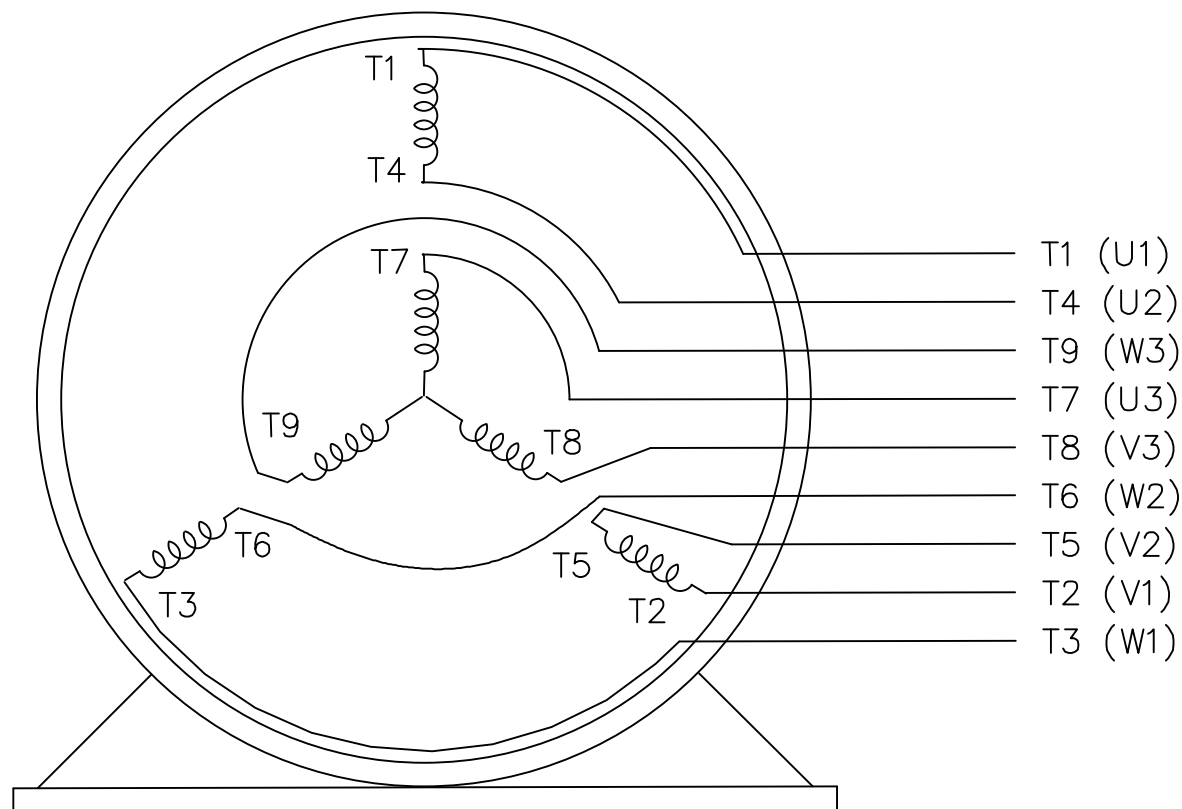
The final state is measured on qubits T1, T2, and T3, with results L1, L2, and L3 respectively.

Diagram illustrating a 3-to-1 multiplexer structure with three data inputs (L1, L2, L3) and one data output (L4). The inputs are connected to two multiplexers each, and the outputs are connected to two multiplexers each.

Inputs and their corresponding multiplexers:

- (U1) T1 and (U3) T7 connect to the first multiplexer for L1.
- (V1) T2 and (V3) T8 connect to the second multiplexer for L1.
- (W1) T3 and (W3) T9 connect to the first multiplexer for L2.
- (U2) T4 and (V2) T5 connect to the second multiplexer for L2.
- (V2) T5 and (W2) T6 connect to the first multiplexer for L3.
- (U2) T4 and (U3) T7 connect to the second multiplexer for L3.


Outputs are labeled L1, L2, L3, and L4.



VIEW OF TERMINAL END

REF.  
WINDING DIAGRAM

T8Y, T2Y, T2BL, T4BX, T2EC, T2G  
T6BZ, T2B, T6BL, T4AV, T6B, T4B

				TOLERANCES UNLESS SPECIFIED		 ELECTRIC MOTORS GEARMOTORS AND DRIVES	DRAWN HLB 04-29-2002		
				DEC.	INCHES		CHK ML 05-03-2002		
				.X	±.1		APPD GK 05-03-2002		
				.XX	±.01	TITLE CONNECTION DIAGRAM 3ø – DUAL VOLTAGE MOTOR	SCALE 1=1		
2	ADDED IEC NOTATIONS... (U1), (V1) ETC. (MU105786)	REP 01-11-2012	DR	.XXX	±.005		REF		
1	NEW DRAWING	HLB 05-03-2002	ML	.XXXX	±.0005	MAT'L.	FMF		
NO.	REVISION	BY & DATE	CHK	ANG	±1/2"	FINISH	PREV		
THIS DRAWING IN DESIGN AND DETAIL IS OUR PROPERTY AND MUST NOT BE USED EXCEPT IN CONNECTION WITH OUR WORK ALL RIGHTS OF DESIGN AND INVENTION ARE RESERVED THIS IS AN ELECTRONICALLY GENERATED DOCUMENT – DO NOT SCALE THIS PRINT				RFP		CAD FILE EE7308-LE	SIZE A	DRAWING NO. EE7308-LE	PAGE OF 2
				DIST LB-WP					